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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jiong-Ping Lu, *et al.*

Serial No.: 10/810,759

Filed: March 26, 2004

Title: **A METHOD FOR MANUFACTURING A SEMICONDUCTOR DEVICE HAVING A SILICIDED GATE ELECTRODE AND A METHOD FOR MANUFACTURING AN INTEGRATED CIRCUIT INCLUDING THE SAME**

Grp./A.U.: 2818

Examiner: Chuong A. Luu

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Sir:

AMENDMENT UNDER 37 C.F.R. § 1.111

In response to the Examiner's Action mailed November 23, 2005, please amend the above-identified application as follows: